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P.V. Brennan, Phase-Locked Loops: Principles and Practice. MacMillan Press Ltd., Great Britain, 1996.

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Application of digital system with clock speed in

Phase locked-loops in the 1-Ghz range relying on the Chip of a Phase Locked Loop Clock Multipliers locked loops, Principles and practice

Prevention of islanding in grid-connected

Recommended Practice for utility Interface Phase-Locked Loops P. V. Brennan, Phase-Locked Loops: Principles and Practice, Macmillan Press, New

Patent us6417738 - adaptive frequency-hopping

aforesaid Lautzenhiser patents. A phase-locked oscillator is attested to by P.V. Brennan in Phase-Locked Loops, Principles practice the present

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United states patent (10) patent n0.: us 7,042,971

United States Patent (10) Patent N0 by 1131 days_ P.V. Brennan, Phase Locked Loops: Principles and Practice , Phase Locked Loop For High Speed

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Phase-locked loop

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Prof Paul Brennan. PhD, CEng, FIET. Professor of Microwave Electronics. Room 1110, Roberts Building, UCL. Phase-locked loops, principles and practice. UK: Macmillan.

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Phase-Locked Loops: Principles and Practice (1996) by P. V. Brennan, Radiation hardness is an important consideration for Phase Locked Loops

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